

IN THE SPECIFICATION:

At page 7, replace the paragraph beginning at line ⁶~~12~~ with the following:

A2 As described below with reference to Figure 7, microprocessor 112 processes these stored pixel sets using a program stored in read only memory (ROM) 234 of the memory 114 and using a random access memory area 232 of the memory 114 to produce correction coefficients for the correction memory 116, shown in Figure 1 and to store coefficients and tally RAM images for the various lens conditions (e.g. zoom, focus and aperture settings).

Please replace the paragraph beginning at page 12, line ¹⁷~~24~~ with the following:

A3 The signal EDGE is applied to a second input terminal of the AND gate 411 and to the reset input terminal of a 32 pixel counter 420. The output signal of the AND gate 411 is applied to the set input terminal, S, of the flip flop 412 and the carry out signal of the 32 pixel counter 420 is applied to the reset input terminal of the flip-flop 412. Thus the flip-flop 412 is set when an edge is detected and reset when the counter 420 has counted 32 samples following that edge. The output signal of the flip flop 412, an inverted signal R SEL, and the output data provided by the tally RAM 224, shown in Figure 2, are applied to respective input terminals of an AND gate 414. The output signal of this AND gate is the video RAM write enable signal. This signal is also applied to an enable input terminal of the 32 pixel counter 420. The counter 420 is coupled to count pulses of the signal CLOCK when it is enabled. When the counter 420 reaches a value of 32, the carry out signal resets the flip-flop. The carry out signal is also applied to an AND gate 413 along with the output signal of the color balance circuitry. If the output signal of the balance counter is logic-high, then, when the carry out signal is pulsed, the AND gate 413 generates a signal NEW SAMPLE, indicating that a new set of samples has been written into the video RAMs 228 and 230 (shown in Figure 2). The signal NEW SAMPLE, increments the more significant bits of the address value applied to the video RAMs, so that the next sample set stored in a new location.

IN THE CLAIMS:

Please replace claims 1-2, 4-10, and 13-15 with the following amended claims: